

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
2	BRS	L2	250	(analog near2 PWM or pulse adj2 width\$3 near3 analog or "D/A" near3 PWM or pulse adj2 width\$2 near3 "d/a" or PWM near3 "DAC" or pulse adj2 width\$3 near3 "DAC" or discrete near3 PWM or pulse adj2 width\$3 near3 discrete) same (feedback or loop or feedback or loopfilter\$3)	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 21:33	
3	BRS	L3	1075	(add\$3 or summ\$3 or combin\$4) with (integra\$6 near3 analog or integra\$6 near3 "DAC" or integra\$6 near3 "DA" or sigma\$1 near2 delta\$1 near3 integra\$6)	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:56	
4	BRS	L4	5	2 and 3	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:47	
5	BRS	L5	2214	(add\$3 or summ\$3 or combin\$4) same (integra\$6 near3 analog or integra\$6 near3 "DAC" or integra\$6 near3 "DA" or sigma\$1 near2 delta\$1 near3 integra\$6)	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:51	
6	BRS	L6	7	2 and 5	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:52	



	Error Definition	Err ors
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L7	509	(feedback or feedback or loop or loopfilter\$3) same (sigma or delta near3 conver\$4) same (integra\$6) same (discrete or DAC or "D/A" or "DA" or analog adj2 conver\$6)	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:55	
8	BRS	L10	169	5 and 7	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:55	
9	BRS	L8	2	2 and 7	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:55	
10	BRS	L9	106	3 and 7	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:56	
11	BRS	L11	106	(add\$3 or summ\$3 or combin\$4 or subtract\$4) and 9	US-PGPUB ; USPAT ; EPO; JPO	2005/07/22 20:57	
12	BRS	L12	0	("2005/0093726").URPN.	USPAT	2005/07/22 20:57	
13	BRS	L13	0	("6765520").URPN.	USPAT	2005/07/22 21:01	
14	BRS	L14	0	("6765520").URPN.	USPAT	2005/07/22 21:02	



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7		
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
15	BRS	L15	6	("5012244"   "5977895"   "6064326"   "6362763"   "6489907"   "6522277").PN.	US- PGPUB ; USPAT ; USOCR	2005/07/2 2 21:02	
16	BRS	L16	10	("6362763").URPN.	USPAT	2005/07/2 2 21:04	
17	BRS	L17	0	("6515604").URPN.	USPAT	2005/07/2 2 21:07	
18	BRS	L18	13	("5055846"   "5243345"   "5274375"   "5392042"   "5420892"   "5724037"   "5757300"   "5805093"   "5859603"   "6064326"   "6307493"   "6362763"   "6404369").PN.	US- PGPUB ; USPAT ; USOCR	2005/07/2 2 21:17	
19	BRS	L19	62	10 not 11	US- PGPUB ; USPAT ; USOCR	2005/07/2 2 21:18	
20	BRS	L20	59	(PWM or pulse adj2 width\$3 or discrete) and (9 or 11 or 15 or 16 and 18)	US- PGPUB ; USPAT ; EPO; JPO	2005/07/2 2 21:22	
21	BRS	L25	16	(discrete near3 PWM or pulse adj2 width\$3 near3 discrete) same (feedback or loop or feedback or loopfilter\$3)	US- PGPUB ; USPAT ; EPO; JPO	2005/07/2 2 21:33	



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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
45	BRS	L49	1	kalb-arthur-joseph.in.	US- PGPUB ; USPAT ; EPO; JPO	2005/07/2 2 22:01	



	Error Definition	Err ors
45		



Day : Friday  
Date: 7/22/2005

Time: 22:04:11


**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = KALB

First Name = ARTHUR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60659476</a>	Not Issued	020	03/07/2005	ACCURATE CASCODE BIAS NETWORKS	KALB, ARTHUR J.
<a href="#">60558401</a>	Not Issued	159	03/31/2004	DIFFERENTIAL STAGE VOLTAGE OFFSET TRIM CIRCUITRY	KALB, ARTHUR J.
<a href="#">60330043</a>	Not Issued	159	10/16/2001	CLASS A/B AMPLIFIER ARCHITECTURE	KALB, ARTHUR J.
<a href="#">11098904</a>	Not Issued	030	04/04/2005	ACCURATE CASCODE BIAS NETWORKS	KALB, ARTHUR JOSEPH
<a href="#">10911371</a>	Not Issued	030	08/03/2004	DIFFERENTIAL STAGE VOLTAGE OFFSET TRIM CIRCUITRY	KALB, ARTHUR J.
<a href="#">10272172</a>	<a href="#">6714076</a>	150	10/15/2002	BUFFER CIRCUIT FOR OP AMP OUTPUT STAGE	KALB, ARTHUR J.
<a href="#">09556607</a>	Not Issued	071	04/21/2000	APPARATUS AND METHOD FOR CONVERTING ANALOG SIGNAL TO PULSE-WIDTH-MODULATED SIGNAL	KALB, ARTHUR JOSEPH
<a href="#">08955904</a>	<a href="#">5923275</a>	150	10/22/1997	ACCURATE CHARGE-DIVIDING DIGITAL-TO-ANALOG CONVERTER	KALB, ARTHUR J.
<a href="#">08928036</a>	<a href="#">5939938</a>	150	09/11/1997	AMPLIFIER CIRCUIT WITH REDUCED DC POWER RELATED TURN-ON AND TURN-OFF TRANSIENTS	KALB, ARTHUR J.
<a href="#">08609726</a>	<a href="#">5703529</a>	150	03/01/1996	AMPLIFIER CIRCUIT WITH REDUCED DC POWER RELATED TRANSIENTS	KALB, ARTHUR J.

**Inventor Search Completed: No Records to Display.**

Search Another: Inventor

Last Name	First Name	
<input type="text" value="KALB"/>	<input type="text" value="ARTHUR"/>	<input type="button" value="Search"/>



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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
47	BRS	L51	4	kalb-arthur-\$.in.	US- PGPUB ; USPAT ; EPO; JPO	2005/07/2 2 22:05	



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47		